



AKRG COLLEGE OF ENGINEERING & TECHNOLOGY

(Approved By A.I.C.T.E., New Delhi and Affiliated to JNTUK, KAKINADA)

NALLAJERLA, W.G.Dt., (A.P)-534112

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

INTEGRATED CIRCUITS & PULSE AND DIGITAL CIRCUITS LABORATORY

List of Equipments:

S No	Name Of The Equipment	Quantity
1	CROs(MX)	4
2	CROs(EZ)	5
3	Function Generators	10
4	RPS	10
5	Sampling Gates	1
6	Astable Multivibrator	1
7	Monostable Multivibrator	1
8	Bistable Multivibrator	1
9	Schmitt Trigger	1
10	UJT Relaxation Oscillator	1
11	Bootstrap Sweep Circuit	1
12	Phase Locked Loop	1
13	Study of Logic Gates	1
14	Integrator and Differentiator using 741	1
15	Voltage Regulator	1
16	Inverter(TTL & CMOS)	1
17	Flip Flop Trainer	1
18	Decade Counter	1
19	BCD to 7 Segment Interface	1
20	4 bit DAC	1
21	Sample And Hold Circuits	1
22	DMM	5

Approximate Equipment Cost: 2.87 Lakhs