



# AKRG COLLEGE OF ENGINEERING & TECHNOLOGY

(Approved By A.I.C.T.E., New Delhi and Affiliated to JNTUK, KAKINADA)

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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

III B.Tech – II Semester

Regulation – R13

## DIGITAL SIGNAL PROCESSING LAB

### LIST OF EXPERIMENTS:

1. To study the architecture of DSP chips – TMS 320C 5X/6X Instructions
2. To verify Linear convolution
3. To verify the Circular convolution
4. To design FIR filter (LP/HP) using windowing technique
  - a) Using Rectangular window
  - b) Using Triangular window
  - c) Using Kaiser window
5. To Implement IIR filter (LP/HP) on DSP Processors
6. N-point FFT algorithm
7. MATLAB program to generate sum of sinusoidal signals
8. MATLAB program to find frequency response of analog LP/HP filters
9. To compute Power density spectrum of a sequence
10. To find the FFT of given 1-D signal and plot