



AKRG COLLEGE OF ENGINEERING & TECHNOLOGY

(Approved By A.I.C.T.E., New Delhi and Affiliated to JNTUK, KAKINADA)

NALLAJERLA, W.G.Dt., (A.P)-534112

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

II B.Tech – II Semester

Regulation – R16

ELECTRONIC CIRCUIT ANALYSIS LAB

The students are required to design the electronic circuit and they have to perform the simulation using Multisim/ Pspice/ Equivalent Licensed simulation software tool. Further they are required to verify the result using necessary hardware in the hardware laboratory.

List of Experiments: (Minimum of Ten Experiments has to be performed)

1. Determination of F_t of a given transistor.
2. Voltage-Series Feedback Amplifier
3. Current-Shunt Feedback Amplifier
4. RC Phase Shift/Wien Bridge Oscillator
5. Hartley/Colpitt's Oscillator
6. Two Stage RC Coupled Amplifier
7. Darlington Pair Amplifier
8. Bootstrapped Emitter Follower
9. Class A Series-fed Power Amplifier
10. Transformer-coupled Class A Power Amplifier
11. Class B Push-Pull Power Amplifier
12. Complementary Symmetry Class B Push-Pull Power Amplifier
13. Single Tuned Voltage Amplifier
14. Double Tuned Voltage Amplifier