



# AKRG COLLEGE OF ENGINEERING & TECHNOLOGY

(Approved By A.I.C.T.E., New Delhi and Affiliated to JNTUK, KAKINADA)

NALLAJERLA, W.G.Dt., (A.P.)-534112.

## DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

### TIME - TABLE

Course: B. Tech

Year/Sem: III/II

Date: 15/11/2018

Branch: E.C.E.

w.e.f.:19/11/2018

DAY	9:30AM-10:20AM	10:20AM-11:10AM	11:20AM-12:10AM	12:10AM-1:00PM	1:00PM-2:00PM	2:00PM-2:50PM	2:50PM-3:40PM	3:30PM-4:30PM
	1	2	3	4	LUNCH BREAK	5	6	7
MON	MWE	DC/VLSI LAB				DSP	VLSI	BME(S.H)
TUE	VLSI	MPMC	MWE	IPR		BME	DSP	DSP(S.H)
WED	MPMC	DC/VLSI LAB				BME	DSP	VLSI(S.H)
THU	DSP	VLSI	BME	MWE		MPMC	IPR	MWE(S.H)
FRI	MPMC		DSP	BME		MWE	VLSI	LIB
SAT	BME	VLSI	MPMC	MWE		MPMC LAB		

Subject Abbreviation	Name of the Subject	Name of the Faculty
MPMC	Microprocessors and Microcontrollers	Mr. CH L S S PAVAN KUMAR
DSP	Digital Signal Processing	Miss. U. RAMYA
VLSI	VLSI	Mr. K. ANJI BABU
MWE	Microwave Engineering	Mr. B DURGA PRASAD
BME	Bio-Medical Engineering	Mr. G GAUTAM KRISHNA REDDY
MPMC LAB	Microprocessors and Microcontrollers Lab	Mr. CH L S S PAVAN KUMAR
DC LAB	Digital Communications Lab	Miss. U. RAMYA
VLSI LAB	VLSI Lab	Mr. K. ANJI BABU

*B.P.F.*  
H.O.D.

*[Signature]*  
Principal  
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