



AKRG COLLEGE OF ENGINEERING & TECHNOLOGY

(Approved By A.I.C.T.E., New Delhi and Affiliated to JNTUK, KAKINADA)

NALLAJERLA, W.G.Dt., (A.P)-534112.

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

TIME – TABLE

Course: **M.Tech**

Branch: **ECE**

Academic Year: **2016-17**

Year/Sem: **I/II**

Date: **09/01/2017**

w.e.f.: **18/01/2017**

DAY	9:30AM-10:20AM	10:20AM-11:10AM	11:20AM-12:10AM	12:10AM-1:00PM	LUNCH BREAK	1:50PM-2:40PM	2:40PM-3:30PM	3:30PM-4:20PM
	1	2	3	4		5	6	7
MON	CMOS	IVP	CT&A	ESD	LUNCH BREAK			
TUE	CT&A	ESD	IVP	WCN				
WED	IVP	CMOS	DSPA	WCN				
THU	ESD	WCN	CT&A	DSPA			ADVANCED COMMUNICATION LAB	
FRI	WCN	DSPA	CMOS	ESD				
SAT	DSPA	IVP	CMOS	CT&A				

Subject Abbreviation	Name of the subject	Name of the faculty
CT&A	Coding Theory and Applications	Smt P Saritha
IVP	Image and Video Processing	Miss k Chaitanya Lakshmi
WCN	Wireless Communications and Networks	Mr K Suresh Babu
CMOS	CMOS Analog and Digital IC Design	Mr A.Vasanth Roy
ESD	Embedded System Design	Mr N.B.S. Naveen
DSPA	DSP Processors and Architectures	Mr V.Naresh
AC Lab	Advanced Communications Lab	Mr A Vasanth Roy & Mr N.B.S.Naveen

Sd/-
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