



AKRG COLLEGE OF ENGINEERING & TECHNOLOGY

(Approved By A.I.C.T.E., New Delhi and Affiliated to JNTUK, KAKINADA)

NALLAJERLA, W.G.Dt., (A.P)-534112.

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

Course: B.Tech

Year/Sem: III/II

Date: 24/NOV/2015

Room No: C-302

w.e.f.: 30/NOV/2015

TIME – TABLE

DAY	9:30AM-10:20AM	10:20AM-11:10AM	11:20AM-12:10AM	12:10AM-1:00PM	LUNCH BREAK	1:00PM-1:50PM	1:50PM-2:40PM	2:40PM-3:30PM	3:30PM-4:20PM
	1	2	3	4		5	6	7	
MON	DC	MWE	BME	MPMC	LUNCH BREAK	DSP LAB/MPMC LAB			
TUE	MPMC	DSP	DC	LIB		DC LAB/DSP LAB			
WED	MWE	MPMC	BME	DSP		DC	SS		
THU	DSP	MPMC	MWE	DC		BME	SEMINAR		
FRI	MPMC	MWE	DSP	BME		MPMC LAB/ DC LAB			
SAT	BME	DC	SEMINAR			MWE	DSP	SPORTS	

Subject Abbreviation	Name of the Subject	Name of the Faculty
MPMC	MICROPROCESSORS AND MICROCONTROLLERS	Mr B DURGA PRASAD
DSP	DIGITAL SIGNAL PROCESSING	Mr A VASANTH ROY
MWE	MICROWAVE ENGINEERING	Mr K PRASANTH
DC	DIGITAL COMMUNICATIONS	Mr V NARESH
BME	BIO MEDICAL ENGINEERING	Mr G GAUTAM KRISHNA REDDY
SEMINAR	SEMINAR	Mr N S N MURTHY & Mr CH L S S PAVAN KUMAR
SS	SOFT SKILLS	Miss G APARNA
MPMC LAB	MICROPROCESSORS AND MICROCONTROLLERS LAB	Mr B DURGA PRASAD & Miss K SIRISHA
DSP LAB	DIGITAL SIGNAL PROCESSING LAB	Mr A VASANTH ROY & Mr G GAUTAM KRISHNA REDDY
DC LAB	DIGITAL COMMUNICATIONS LAB	Mr V NARESH & Mr K PRASANTH

Sd/-
PRINCIPAL